



The National Education Society of Karnataka (Regd.)
Basavanagudi, Bengaluru-04

THE NATIONAL COLLEGE
(AUTONOMOUS)
Jayanagara, Bengaluru-560070

DEPARTMENT OF ELECTRONICS

Proceedings of the BOS
Held on

6TH NOVEMBER 2021

THE NATIONAL COLLEGE


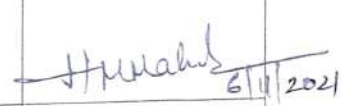





AUTONOMOUS

JAYANAGARA, BENGALURU-70

DEPARTMENT OF ELECTRONICS

Meeting of the Board of studies

Held on 6th November 2021

SL NO	NAME	DESIGNATION	SIGNATURE
1	CHELUVAPPA.S. Associate Professor and H.O.D of Electronics The National College Jayanagar, Bengaluru-70	Chairmen	
2	Dr.H M MAHESH Professor and Chairman, Department of Electronic Science, Bangalore University, JnanaBharathi, Bangalore - 560056	University Nominee	 6/11/2021
3	MAHADEVA.M Associate Professor, Department of Electronics The National College Jayanagar, Bengaluru-70	Faculty Member	
4	Dr. MANJESH Professor Department of Electronic Science J.B. Campus, Bangalore University, Bengaluru	Subject Expert	
5	Dr.UMA ULLAS PRADHAN Associate Professor Centre for Incubation and Research and Consultancy Jyothi Institute of Technology, Bengaluru 82.	Subject Expert	
6	Sri VIKAS SHASTRI. Product Development Engineer. Intel Technology India Pvt Ltd, Bengaluru	Industry Representative	
7	Sri SHREEVATSA Embeded Engineer. Dishi Systems Pvt Ltd, Bengaluru	Alumni Representative	

A meeting of the B.O.S was held in the Board Room, The National College, Jayanagar on Saturday 6th November 2021 at 10.30 AM. Sri. Cheluvappa S, Chairman and H.O.D of Electronics, The National College Jayanagar welcomed the members. He read the proceedings of the last meeting of the Board of studies held on 29th August 2020. He presented the revised draft syllabus for the **First** and **Second** semesters of Autonomous B.Sc Electronics Course from 2021-2022 onwards.

RESOLUTIONS

1. The BOS resolved to adapt the B.Sc Electronics Syllabus of Bangalore university with minor modifications.
2. Dr Manjesh, professor, Department of Electronics Sciences, Bangalore University jnana Bharathi, Bangalore and Dr.Uma Ullas Pradhan, Associate professor, Centre for Incubation and Research and Consultancy Bangalore, suggested to include one of the network analysis methods in the Chapter 2 of the First Semester Discipline Specific Core paper. It was decide to include mesh current method of network analysis.
3. Dr.Uma Ullas Pradhan, suggested to include *Power Audit* in the first semester DSC.
4. It was also decided to include *Transistor as a switch* in the DSC of the first Semester.
5. It was decided to retain basics of MOSFET in the second semester. Power MOSFET and IGBT to be included in the Power Electronics Paper of Sixth Semester.
6. It was decided to keep any five experiments from part A and any five experiments from Part B of Practical for performance
7. The BOS also approved to include Digital Computer Electronics and Microprocessor and Programming as Open electives for First and Second Semesters respectively along with the Open electives, proposed by the Bangalore University.
8. The BOS finally approved the structure and modified syllabus
9. The BOS also decided to approve The Board of Examiners (BOE) panel for the academic year 2021-2022.
10. Chairman finally thanked the members for their co-operation.



CHELUVAPPA .S

Date: 6-11-2021

Head of The Dept of Electronics
The National College,
(Autonomous)
Jayanagar, Bengaluru - 560 070

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**COURSE PATTERN AND SCHEME OF EXAMINATION for
B.Sc. / B.Sc. (Hons.) as per NEP (2021-22 and onwards)**

Semester	Title of the Paper	Teaching Hours	Hours /week		Examination Pattern Max.Marks /Paper				Duration Of Exam (hours)		Total Marks / paper	Theory Credits	Practical Credits
			Theory	Practical	Theory		Practical		Theory	Practical			
					Exam	IA	Exam	IA					
I	1SELE1T: Electronic Devices and Circuits	56	4	4	60	40	25	25	3	4	150	4	2
	1ELEOE 1.1/1.2/1.3/1.4/1.5	42	3	-	60	40	-	-	3*	-	100	3	
II	2SELE2T: Analog and Digital Electronics	56	4	4	60	40	25	25	3	4	150	4	2
	2ELEOE 2.1/2.2/2.3/2.4/2.5	42	3	-	60	40	-	-	3*	-	100	3	



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