

THE NATIONAL COLLEGE







AUTONOMOUS

JAYANAGARA, BENGALURU-70

DEPARTMENT OF ELECTRONICS

Meeting of the Board of studies

Held on 29th August 2020

SL NO	NAME	DESIGNATION	SIGNATURE
1	CHELVAPPAS Associate Professor and H.O.D of Electronics The National College Jayanagar, Bengaluru-70	Chairmen	
2	Dr. J T DEVARAJU Registrar (Evaluation) Bangalore University, Bengaluru	University Nominee	 29/8/2020
3	MAHADEVAM Associate Professor Department of Electronics The National College Jayanagar, Bengaluru-70	Faculty Member	 M. Mahadevam
4	Dr. MANJESH Associate Professor Department of Electronic Science J.B. Campus, Bangalore University, Bengaluru	Subject Expert	
5	Dr.M R. Rashmi Associate Professor Department of Electronics Amrutha School of Engineering. Amrutha Visvavidyapetam, Bengaluru.	Subject Expert	
6	Sri Sripadaraj. MEMS Technical Consultant IntelliSense Software Pvt Ltd Bengaluru-560076	Industry Representative	
7	Sri Goutham Anand Research Assistant IISc Bengaluru	Alumni Representative	

A meeting of the B.O.S was held in the department of Electronics, The National College, Jayanagar on Saturday 29th August 2020 at 9.30 AM. Sri. Cheluvappa S, H.O.D of Electronics, The National College Jayanagar welcomed the members. He read the proceedings of the last meeting of the Board of studies held on 30th May 2019. He presented the revised draft syllabus for the Fifth and Sixth semesters of Autonomous Electronics Course from 2020-2021 onwards.

The following were the contents of Fifth and Sixth semester B.Sc Electronics syllabus

Fifth Semester

Paper 5SELE5T– Microcontrollers and programming

Paper 5SELE5P – Microcontroller Lab

Paper 5SELE6T– Analog Communication

Paper 5SELE6P – Analog Communication Lab

Sixth Semester

Paper 6SELE7T – Verilog and VLSI

Paper 6SELE7P – Verilog HDL and Advanced Communication Lab


Paper 6SELE8T – Advanced Communication

Paper 6SELE8P – Project Lab

1. The draft Syllabus was Scrutinized.
2. Dr J T Devaraju Registrar (Evaluation) Bangalore University jnana Bharathi Bangalore suggested restructuring of the B.Sc Electronics Syllabus.
3. It was decided that the restructuring of the B.Sc Electronics Syllabus can be done from the next academic year starting from First Semester.
4. Dr J T Devaraju suggested to include Interfacing of LCD in the paper 5SELE5T.
5. He also suggested to include more Interfacing related programs in the paper 5SELE5P.
6. Members also suggested reducing the Television Unit in the paper 6SELE5T.

7. Dr J T Devaraju suggested to restructure the Digital communication unit in the Paper 6SELE8T .
8. Sri Goutham Anand Suggested to Include I²C and CAN protocols in the Syllabus.
9. Suggested changes were incorporated in the syllabus.
10. The syllabus was approved.
11. The BOS also decided to approve The Board of Examiners (BOE) panel for the academic year 2020-2021.
12. Chairman finally thanked the members for their co-operation.

Date: 29-8-2020


CHELUVAPPA .S
H.O.D OF ELECTRONICS
THE NATIONAL COLLEGE
JAYANAGARA, BENGALURU -70
Head of The Dept of Electronics
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Jayanagar, Bengaluru - 560 070

VI Semester B.Sc

PAPER: 6SELE8P: Project Lab

Max Marks:35


No. Of Hours/Week:3

About The Course

6SELE8P is designed to serve as an eighth course in an undergraduate B.Sc Electronics Laboratory curriculum.

PROJECT

- ❖ Students in a group, not exceeding THREE, should Design, Fabricate and Assemble ONE Electronics project. The Department Faculty is required to guide the project work.
- ❖ Each student should prepare a report and submit at the time of practical examination viva voce duly certified by the concerned Faculty & HOD.
- ❖ Department Faculty shall ensure that the entire project work is carried out in the practical class assigned to practical VIII and the students shall be required to give the Seminar on the project.


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